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09/978,497	10/16/2001	Jung-Hwan Choi	9898-199	7318

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EXAMINER

DUONG, KHANH B

ART UNIT	PAPER NUMBER
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2822

DATE MAILED: 01/27/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

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DETAILED ACTION

Response to Amendment

This Office Action is in response to the amendment filed November 8, 2004.

Accordingly, claims 8, 9 and 30 were canceled, and claims 2, 26, 27 and 29 were amended.

Claims 32-41 remain withdrawn from consideration as being directed to a non-elected invention.

Currently, claims 2, 4, 26, 27, 29 and 32-41 are pending.

Claim Objections

Claim 29 is objected to under 37 CFR 1.75 as being a substantial duplicate of claim 26. When two claims in an application are duplicates or else are so close in content that they both cover the same thing ("second memory module in the same direction"), despite a slight difference in wording, it is proper after allowing one claim to object to the other as being a substantial duplicate of the allowed claim. See MPEP § 706.03(k).

Appropriate correction is required.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

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The factual inquiries set forth in *Graham v. John Deere Co.*, 383 U.S. 1, 148 USPQ 459 (1966), that are applied for establishing a background for determining obviousness under 35 U.S.C. 103(a) are summarized as follows:

1. Determining the scope and contents of the prior art.
2. Ascertaining the differences between the prior art and the claims at issue.
3. Resolving the level of ordinary skill in the pertinent art.
4. Considering objective evidence present in the application indicating obviousness or nonobviousness.

This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

Claims 2, 26, 27 and 29 are rejected under 35 U.S.C. 103(a) as being unpatentable over Koyama et al. (US 6,382,983).

Koyama et al. ("Koyama") discloses in FIGs. 1-22 a through socket comprising: a socket body (2 and 3) arranged to load first and second memory modules (6 and 23) in the same direction (see FIG. 21) or opposite direction (see FIG. 17); a first conductor 10 arranged to connect a plurality of adjacent contacts 7 on a first surface 13 of the first memory module 6 to a plurality of adjacent contacts 20 on a first surface 13 of the second memory module 23; and a second conductor 11 arranged to connect a plurality of adjacent contacts 8 on a second surface

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15 of the first memory module 6 to a plurality of adjacent contacts 21 on a second surface 15 of the second memory module 23.

Re claims 2, 26, 27 and 29, Koyama fails to disclose the socket body being detached from the circuit board.

It would have been obvious to one having ordinary skill in the art at the time the invention was made to provide a socket body being detached from the circuit board, since it has been held that constructing a formerly integral structure in various elements involves only routine skill in the art. *Nerwin v. Erlichman*, 168 USPQ 177, 179.

Allowable Subject Matter

Claim 4 is allowed.

The following is a statement of reasons for the indication of allowable subject matter: none of the prior art of record fairly shows or suggests a through socket comprising: a through socket body arranged to load a first memory module, a second memory module, and a third memory module, said first, second and third memory modules being loaded in a base socket mounted to a board; a first conductor arranged to connect a contact on a first surface of the first memory module to a contact on a first surface of the second memory module; a second conductor arranged to connect a contact on a second surface of the second memory module to a contact on the first surface of the third memory module; and a third conductor arranged to connect a contact on a second surface of the first memory module to a contact on a second surface of the third memory module; wherein the through socket is structured to load said memory modules either above or to the side of said base socket mounted on said board.

Response to Arguments

Applicant's arguments filed November 8, 2004 have been fully considered but they are not persuasive.

Applicant argues that Koyama fails to disclose a socket that is detached from and not secured to a motherboard or circuit board. In response, the Examiner respectfully disagrees, because it has been held that constructing a formerly integral structure in various elements involves only routine skill in the art. *Nerwin v. Erlichman*, 168 USPQ 177, 179.

Applicant further argues that the wiring structures in Koyama are not “conductors” as claimed. The Examiner respectfully disagrees because Koyama states that the wiring structures are also being referred to as “wiring conductors” [see col. 1, ln. 17-27].

Conclusion

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event,


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however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Khanh Duong whose telephone number is (571) 272-1836. The examiner can normally be reached on Monday - Thursday (9:00 AM - 6:00 PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amir Zarabian can be reached on (571) 272-1852. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



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